

Customer No.: 31561  
Docket No.: 09919-US-PA-1  
Application No.: 10/711,624

### In the Claims

Please amend the claims as follows.

1. (currently amended) A method of operating a silicon-on-insulator device, wherein the silicon-on-insulator device includes a transistor and a control transistor such that the transistor and the control transistor share a common source region and the drain region of the control transistor is electrically connected to the main body of the transistor, the operating method comprising the following steps:

switching the transistor on by:

applying a bias voltage  $V_{cc}$  to the drain terminal ~~of the transistor~~ and ~~applying a bias voltage  $V_{cc}$  to the gate terminal of the transistor so that~~ and applying 0V to the source terminal of the transistor and the gate terminal of the control transistor ~~are at 0V~~, thereby terminating any electrical connection between the main body of the transistor with the source terminal of the transistor so that the transistor has a characteristic of floating-body silicon-on-insulator device; and

switching the transistor off by:

applying ~~[[a]]~~ the bias voltage  $V_{cc}$  to the drain terminal of the transistor ~~and the gate terminal of the control transistor and applying 0V to the gate terminal and source terminal of the transistor, so that the gate terminal and the source terminal of the transistor are at 0V and applying a bias voltage  $V_{cc}$  to the gate terminal of the control transistor, thereby forming an electrical connection between the main body of the transistor and the source terminal of the~~

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transistor so that the transistor has a characteristic of non-floating body silicon-on-insulator device.